An introduction to al baka part 1 – November 5th, 2024

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CERN - EP/CMD

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who am I



- Dr. Andrea Bocci <andrea.bocci@cern.ch>, @fwyzard on Mattermost
 - applied physicist working on the CMS experiment for over 20 years
 - at CERN since 2010
 - I've held various roles related to the High Level Trigger
 - started out as the b-tagging HLT contact
 - joined as (what today is called) HLT STORM convener
 - deputy Trigger Coordinator and Trigger Coordinator
 - HLT Upgrade convener, and editor for the DAQ and HLT Phase-2 TDR
 - currently, "GPU Trigger Officer"
 - for the last 6 years, I've been working on GPUs and *performance portability*
 - together with a few colleagues at CERN and Fermilab
 - "Patatrack" pixel track and vertex reconstruction running on GPUs
 - R&D projects on CUDA, Alpaka, SYCL and Intel oneAPI
 - support for CUDA, HIP/ROCm, and Alpaka in CMSSW
 - Patatrack Hackathons !

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performance portability



what is *portability*?



- what do we mean by software *portability*?
 - the possibility of running a software application or library on different platforms
 - different hardware architectures, different operating systems
 - e.g. Windows running on x86, OSX running on ARM, Linux running on RISC-V, *etc*.
- how do we achieve software portability?
 - write software using a standardised language
 - C++, python, Java, *etc.*
 - use standard features
 - IEEE floating point numbers
 - use standard or portable libraries
 - C++ standard library, Boost, Eigen, *etc*.

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portability: an example



```
for example
                   https://github.com/fwyzard/intro to alpaka/blob/master/portability/00 hello world.cc
  #include <cmath>
  #include <cstdio>
  void print sqrt(double x) {
    printf("The square root of %g is %g\n", x, std::sqrt(x));
  int main() {
    print sqrt(2.);
```

should behave in the same way on all platforms that support a standard C++ compiler:

The square root of 2 is 1.41421

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what about GPUs?



- writing a program that offloads some of the computations to a GPU is somewhat different from writing a program that runs just on the CPU
 - inside a single application ...
 - ... different hardware architectures
 - ... different memory spaces
 - ... different way to call a function or launch a task
 - ... different optimal algorithms
 - ... different compilers
 - ... different programming languages !
- sometimes it may help to think about a GPU like programming a remote machine
 - compile for completely different targets
 - launching a kernel is similar to running a complete program !





portability: the same example



```
#include <cmath>
                     https://github.com/fwyzard/intro_to_alpaka/blob/master/portability/01_hello_world.cu
#include <cstdio>
#include <cuda runtime.h>
___device__
void print sqrt(double x) {
 printf("The square root of %g is %g\n", x, std::sqrt(x));
global_
void kernel() {
 print sqrt(2.);
int main() {
 kernel<<<1, 1>>>();
 cudaDeviceSynchronize();
The square root of 2 is 1.41421
```

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portability: side by side



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CC

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SA

<pre>#include <cmath></cmath></pre>		#include <cmath></cmath>
<pre>#include <cstdio></cstdio></pre>		#include <cstdio></cstdio>
		<pre>#include <cuda_runtime.h></cuda_runtime.h></pre>
<pre>void print_sqrt(double x) {</pre>		
<pre>printf("The square root of %g is %g\n", x, std::sqrt(x));</pre>		_device
}		<pre>void print_sqrt(double x) {</pre>
		<pre>printf("The square root of %g is %g\n", x, std::sqrt(x));</pre>
<pre>int main() {</pre>		}
<pre>print_sqrt(2.);</pre>		
}		_global
		<pre>void kernel() {</pre>
The square root of 2 is 1.41421		<pre>print_sqrt(2.);</pre>
		}
	6 - C	<pre>int main() {</pre>
ve could		kernel<<<1, 1>>>();
		<pre>cudaDeviceSynchronize();</pre>
 wrap the differences in a few macros or c 	lasses	}
share the common parts	A Share	
	Sell.	The square root of 2 is 1.41421

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so... are we done ?



- not really
 - trivially extending our example to an expensive computation would give horrible performance !
- why?
 - a CPU will run a single-threaded program very efficiently
 - a GPU would perform horribly
 - use a single thread out of O(1k): use *less than 1‰* of its computing power
 - use a single block: loose any possibility of hiding memory latency
 - cannot take advantage of advanced capabilities like atomic operations, shared memory, *etc*.
 - and what about different GPU back-ends ?
- what we need is *performance portability*
 - write code in a way that can run on multiple platforms
 - leverage their potential
 - and achieve (almost) native performance on all of them





performance portability ?





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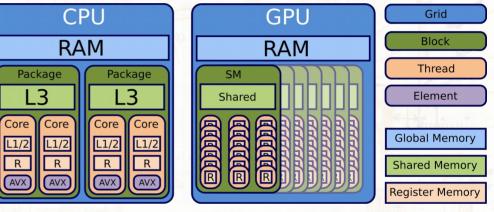
the alpaka performance portability library



what is alpaka?



- alpaka is a header-only C++17 abstraction library for accelerator development
 - it aims to provide *performance portability* across accelerators through the abstraction of the underlying levels of parallelism
- it currently supports
 - CPUs, with serial and parallel execution
 - NVIDIA GPUs, with CUDA
 - AMD GPUs, with HIP/ROCm
 - Intel GPUs and FPGAs, with on SYCL and Intel oneAPI
- it is easy to integrate in an existing project
 - write code once, use a Makefile of CMake to build it for multiple backends
 - a *single application* can supports all the different backends *at the same time*
- the latest documentation is available at https://alpaka.readthedocs.io/en/latest/index.html







setting up alpaka



- download the latest stable version of alpaka from GitHub
 - use version 1.2.0, released on October 2nd 2024, to make sure the examples will work as expected
 - this is a "long term support" release while development moves towards alpaka 2.0.0
 - last version to support c++17

```
# define a directory for the alpaka library
export ALPAKA_BASE=~/private/alpaka
```

clone the latest version of alpaka into a predefined directory
git clone https://github.com/alpaka-group/alpaka \$ALPAKA_BASE -b 1.2.0

- alpaka 2.0.0 will be released in 2025
 - will require c++20
 - will have some breaking changes in the memory and kernel APIs
 - plan to support more modern features like unified memory, cooperative groups, graphs, etc.

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setting up alpaka



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# define a directory for the alpaka library	this part sets up the
export ALPAKA_BASE=~/private/alpaka	environment
<pre># clone the latest version of alpaka into a predefined directory git clone https://github.com/alpaka-group/alpaka \$ALPAKA_BASE -b 1.2.0</pre>	make sure to do it in every session

- alpaka 2.0.0 will be released in 2025
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how does it work?



- Alpaka internally uses preprocessor symbols to enable the different backends:
 - ALPAKA_ACC_GPU_CUDA_ENABLED
 - ALPAKA_ACC_GPU_HIP_ENABLED
 - ALPAKA_ACC_CPU_B_SEQ_T_SEQ_ENABLED

for running on NVIDIA GPUs for running on AMD GPUs for running serially on a CPU

- in this tutorial we will build separate applications from each example
 - each application is compiled with the corresponding compiler (g++, nvcc, hipcc, ...)
 - each application uses a single back-end
- it is also possible to enable more than one back-end at a time
 - however, the underlying CUDA and HIP header files will clash, so one needs to play some tricks with forward declarations, or use separate compilation for the different backends
 - and separate the host and device parts





alsaka core concepts



Host-side API

- initialisation and device selection: Platforms and Devices
- asynchronous operations and synchronisation: Queues and Events
- owning memory Buffers and non-owning memory Views
- submitting work to devices: work division and Accelerators

Device-side API

- plain C++ for device functions and kernels
- shared memory, atomic operations, and memory fences
- primitives for mathematical operations
- warp-level primitives for synchronisation and data exchange (not covered)
- random number generator (not covered)

nota bene:

• most Alpaka API objects behave like shared_ptrs, and should be passed by value or by reference to const (*i.e.* const&)



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platforms and devices



alpaka: initialisation and device selection



Platform and Device

- identify the type of hardware (*e.g.* host CPUs or NVIDIA GPUs) and individual devices (*e.g.* each single GPU) present on the machine
- the CPU device DevCpu serves two purposes:
 - as the "host" device, for managing the data flow (*e.g.* perform memory allocation and transfers, launch kernels, *etc.*)
 - as an "accelerator" device, for running heterogeneous code (*e.g.* to run an algorithm on the CPU)
- platforms and devices should be created at the start of the program and used consistently
 - may hold an internal state, avoid creating multiple instances for the same hardware
- some common cases

back end	alpaka platform	alpaka device
CPUs, serial or parallel	PlatformCpu	DevCpu
NVIDIA GPU, with CUDA	PlatformCudaRt	DevCudaRt
AMD GPUs, with HIP/ROCm	PlatformHipRt	DevHipRt

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platforms and devices O



- Alpaka provides a simple API to enumerate the devices on a given platform:
 - alpaka::getDevCount(platform)
 - returns the number of devices on the given platform
 - alpaka::getDevByIdx(platform, index)
 - initialises the index-th device on the platform, and returns the corresponding Device object
 - alpaka::getDevs(platform)
 - initialises all devices on the platform, and returns a vector of Device objects
 - alpaka::getName(device)
 - returns the name of the given device

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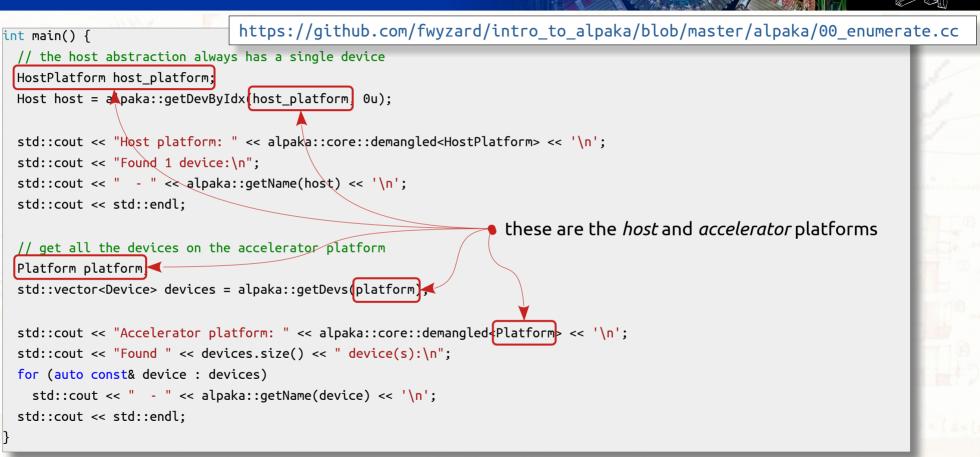


https://github.com/fwyzard/intro_to_alpaka/blob/master/alpaka/00_enumerate.cc int main() { // the host abstraction always has a single device HostPlatform host platform; Host host = alpaka::getDevByIdx(host platform, 0u); std::cout << "Host platform: " << alpaka::core::demangled<HostPlatform> << '\n';</pre> std::cout << "Found 1 device:\n";</pre> std::cout << " - " << alpaka::getName(host) << '\n';</pre> std::cout << std::endl:</pre> // get all the devices on the accelerator platform Platform platform; std::vector<Device> devices = alpaka::getDevs(platform); std::cout << "Accelerator platform: " << alpaka::core::demangled<Platform> << '\n';</pre> std::cout << "Found " << devices.size() << " device(s):\n";</pre> for (auto const& device : devices) std::cout << " - " << alpaka::getName(device) << '\n';</pre> std::cout << std::endl;</pre>

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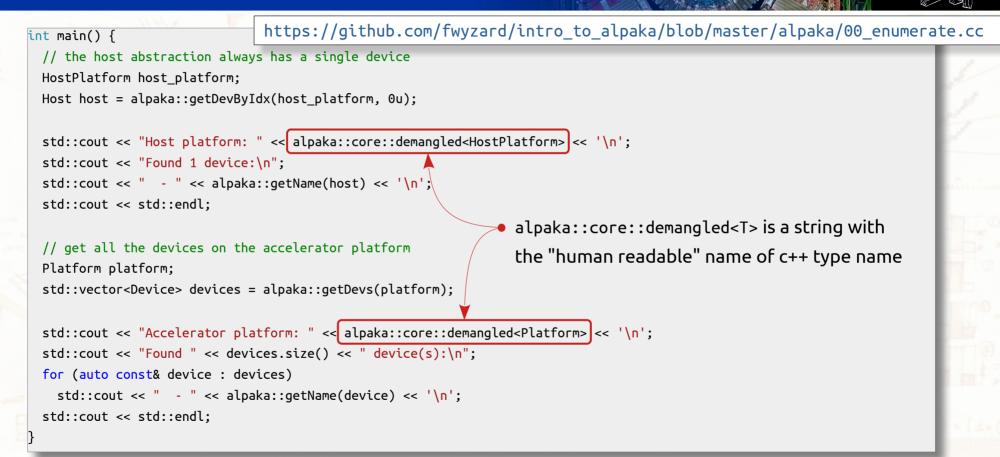




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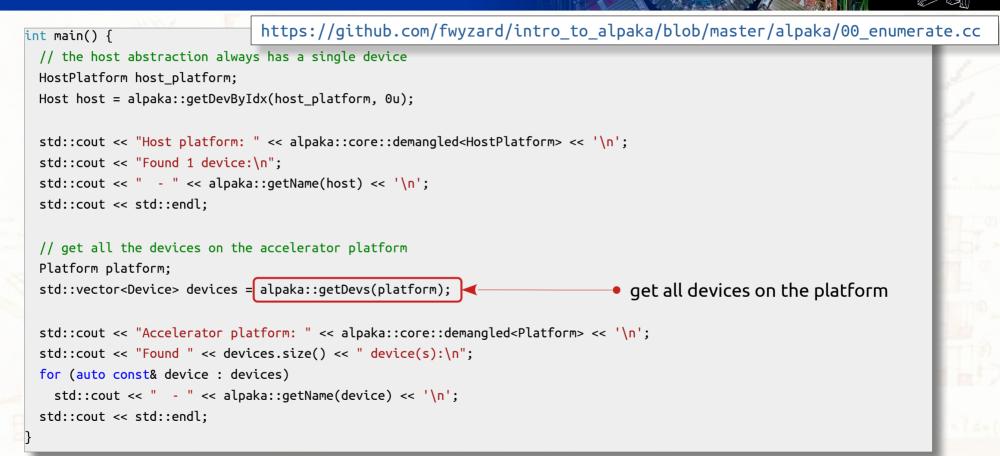


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```
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```

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some important details



```
a++ -std=c++17 -02 -a
    -I$ALPAKA BASE/include -DALPAKA ACC CPU B SEQ T SEQ ENABLED \
    00 enumerate.cc -o 00 enumerate cpu
nvcc -x cu -std=c++17 -02 -g --expt-relaxed-constexpr \
    -I$ALPAKA BASE/include -DALPAKA ACC GPU CUDA ENABLED \
    00 enumerate.cc -o 00 enumerate cuda
#include <iostream>
#include <vector>
#include <alpaka/alpaka.hpp>
#include "config.h"
```

grab all the examples from GitHub

git clone https://github.com/fwyzard/intro_to_alpaka.git

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let's build it ...



- using the CPU as the "accelerator"
 - the CPU acts as both the "host" and the "device"
 - the application runs entirely on the CPU

```
g++ -std=c++17 -02 -g \
    -I$ALPAKA_BASE/include -DALPAKA_ACC_CPU_B_SEQ_T_SEQ_ENABLED \
    00_enumerate.cc \
    -0 00_enumerate_cpu
```

- using the CUDA GPUs as the "accelerator"
 - the CPU acts as the "host", the GPUs act as the "devices"
 - the application launches kernels that run on the GPUs

```
nvcc -x cu -expt-relaxed-constexpr -std=c++17 -02 -g \
    -I$ALPAKA_BASE/include -DALPAKA_ACC_GPU_CUDA_ENABLED \
    00_enumerate.cc \
    -0 00_enumerate_cuda
```

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... and run it



\$./00_enumerate_cpu Host platform: alpaka::PlatformCpu Found 1 device:

- AMD EPYC 7352 24-Core Processor

Accelerator platform: alpaka::PlatformCpu Found 1 device(s):

- AMD EPYC 7352 24-Core Processor

Host platform: alpaka::PlatformCpu

Found 1 device:

- AMD EPYC 7352 24-Core Processor

Accelerator platform: alpaka::PlatformUniformCudaHipRt<alpaka::ApiCuda Rt>

```
Found 2 device(s):
```

- Tesla T4
- Tesla T4

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where is the magic?



#if defined(ALPAKA_ACC_GPU_CUDA_ENABLED)

// CUDA backend
using Device = alpaka::DevCudaRt;
using Platform = alpaka::Platform<Device>;

#elif defined(ALPAKA_ACC_GPU_HIP_ENABLED)

// HIP/ROCm backend

using Device = alpaka::DevHipRt; using Platform = alpaka::Platform<Device>;

#elif defined(ALPAKA_ACC_CPU_B_SEQ_T_SEQ_ENABLED) // CPU serial backend using Device = alpaka::DevCpu; using Platform = alpaka::Platform<Device>;

back endalpaka platformalpaka deviceCPUs, serial or parallelPlatformCpuDevCpuNVIDIA GPU, with CUDAPlatformCudaRtDevCudaRtAMD GPUs, with HIP/ROCmPlatformHipRtDevHipRt

#else

// no backend specified

#error Please define one of ALPAKA_ACC_GPU_CUDA_ENABLED, ALPAKA_ACC_GPU_HIP_ENABLED, ALPAKA_ACC_CPU_B_SEQ_T_SEQ_ENABLED

#endif

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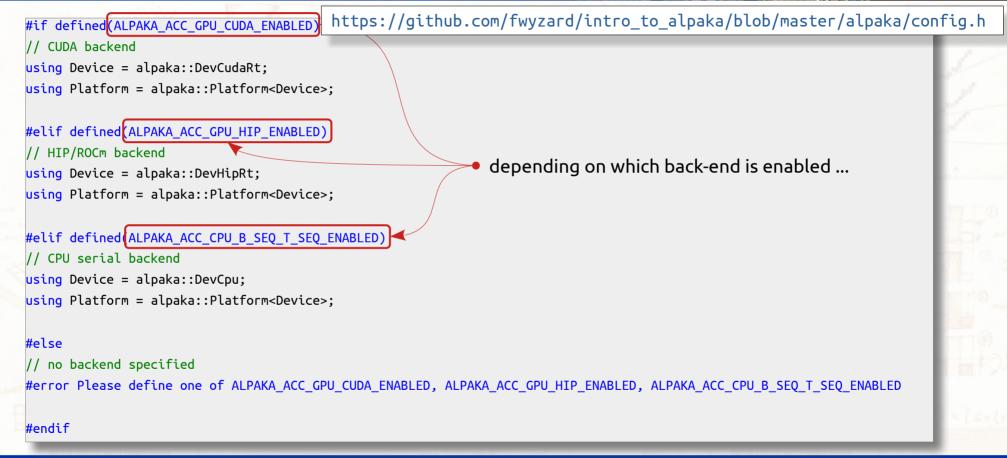


https://github.com/fwyzard/intro_to_alpaka/blob/master/alpaka/config.h



where is the magic?





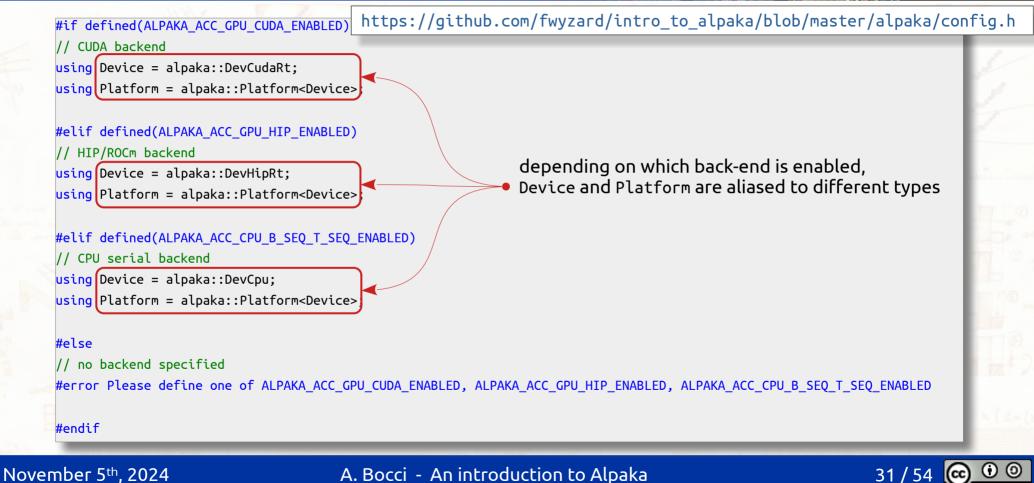
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where is the magic?





queues and events



alpaka: asynchronous operations



Queues:

- identify a "work queue" where tasks (memory operations, kernel executions, ...) are executed in order
 - for example, a queue could represent an underlying CUDA stream or a CPU thread
 - from the point of view of the host , queues can be synchronous or asynchronous
- with a synchronous (or *blocking*) queue:
 - any operation is executed immediately, before returning to the caller
 - the host automatically waits (blocks) until each operation is complete
- with an asynchronous (or *non-blocking*) queue:
 - any operation is executed in the background, and each call returns immediately, without waiting for its completion
 - the host needs to synchronize explicitly with the queue, before accessing the results of the operations
- in general, prefer using a synchronous queue on a CPU, and an asynchronous queue on a GPU
- queues are always associated to a specific device
- most Alpaka operations (memory ops, kernel launches, *etc*.) are associated to a queue
- Alpaka does not provide a "default queue", create one explicitly





common operations on queues



- creating a queue of the predefined type associated to a device is as simple as auto queue = Queue(device);
- waiting for all the asynchronous operations in a queue to complete is as simple as alpaka::wait(queue);
- enqueue a host function

alpaka::enqueue(queue, host_function); alpaka::enqueue(queue, [&]() { ... });

- enqueue a device function (launch a kernel)
 alpaka::exec<Acc>(queue, grid, kernel, args...);
- allocate, set, or copy memory host and device memory

 auto buffer = alpaka::allocAsyncBuf<T, size_t>(queue, size);
 alpaka::memset(queue, buffer, 0x00);
 alpaka::memcpy(queue, destination, source);

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alpaka: events and synchronisation



Events:

- events identify points in time along a work queue
- can be used to query or wait for the readiness of a task submitted to a queue
- can be used to synchronise different queues
- like queues, events are always associated to a specific device



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common operations on events



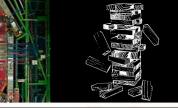
- events associated to a given device can be created with:
 - auto event = Event(device);
- events are enqueued to mark a given point along the queue:
 - alpaka::enqueue(queue, event);
 - an event is "complete" once all the work submitted to the queue before the event has been completed
- an event can be used to block the execution on the host until it is complete: alpaka::wait(event);
 - blocks the execution on the host
- or to make an other queue wait until a given event (in a different queue) is complete: alpaka::wait(other_queue, event);
 - does not block execution on the host
 - further work submitted to other_queue will only start after event is complete
- an event's status can also be queried without blocking the execution: alpaka::isComplete(event);

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more magic



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#if defined(ALPAKA_ACC_GPU_CUDA_ENABL https://github.com/fwyzard/intro_to_alpaka/blob/master/alpaka/config.h
// CUDA backend
using Queue = alpaka::Queue<Device, alpaka::NonBlocking>;
using Event = alpaka::Event<Queue>;

#elif defined(ALPAKA_ACC_GPU_HIP_ENABLED)
// HIP/ROCm backend
using Queue = alpaka::Queue<Device, alpaka::NonBlocking>;
using Event = alpaka::Event<Queue>;

#elif defined(ALPAKA_ACC_CPU_B_SEQ_T_SEQ_ENABLED)
// CPU serial backend
using Queue = alpaka::Queue<Device, alpaka::Blocking>;
using Event = alpaka::Event<Queue>;

#else
// no backend specified
#error Please define one of ALPAKA_ACC_GPU_CUDA_ENABLED, ALPAKA_ACC_GPU_HIP_ENABLED,
ALPAKA_ACC_CPU_B_SEQ_T_SEQ_ENABLED

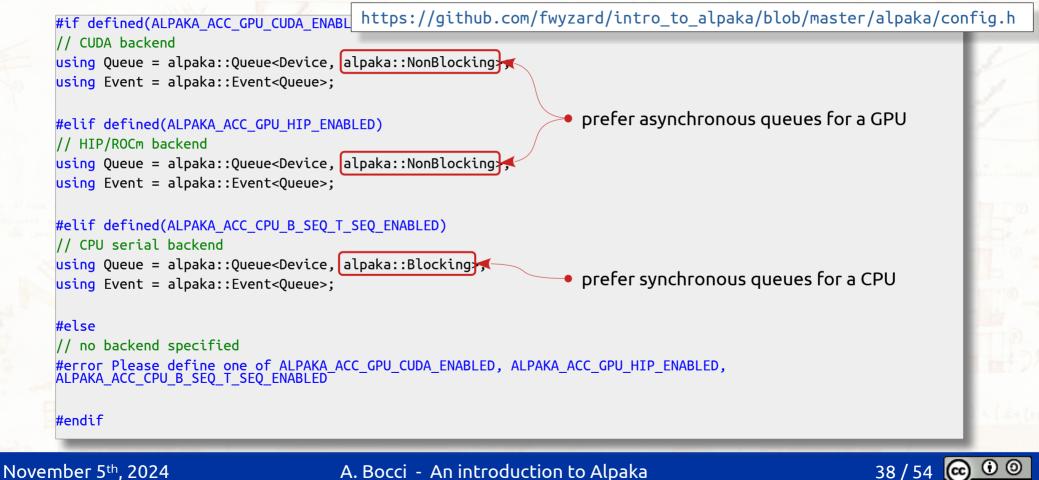
#endif

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more magic









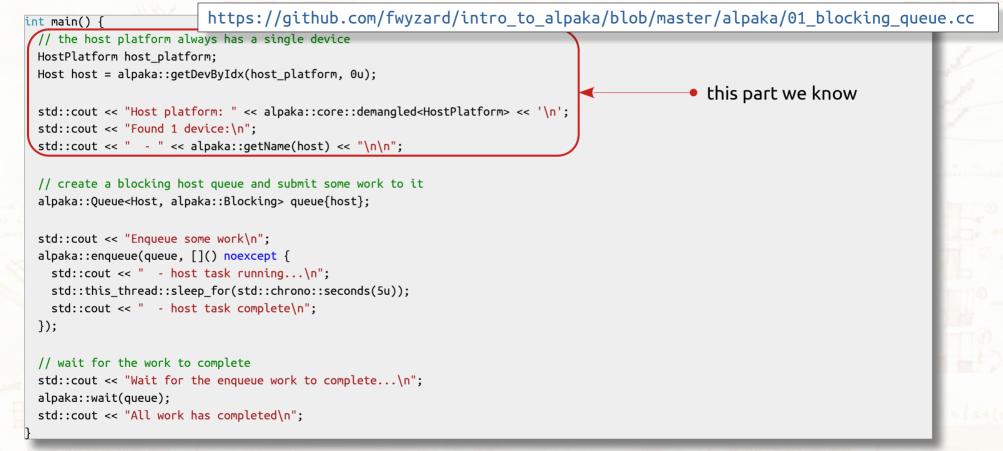
```
https://github.com/fwyzard/intro_to_alpaka/blob/master/alpaka/01_blocking_queue.cc
int main() {
 // the host platform always has a single device
 HostPlatform host platform;
 Host host = alpaka::getDevByIdx(host platform, 0u);
 std::cout << "Host platform: " << alpaka::core::demangled<HostPlatform> << '\n';</pre>
 std::cout << "Found 1 device:\n":</pre>
 std::cout << " - " << alpaka::getName(host) << "\n\n";</pre>
 // create a blocking host queue and submit some work to it
 alpaka::Queue<Host, alpaka::Blocking> gueue{host};
 std::cout << "Enqueue some work\n";</pre>
 alpaka::enqueue(queue, []() noexcept {
   std::cout << " - host task running...\n";</pre>
   std::this thread::sleep for(std::chrono::seconds(5u));
   std::cout << " - host task complete\n";</pre>
 });
 // wait for the work to complete
 std::cout << "Wait for the enqueue work to complete...\n";</pre>
 alpaka::wait(queue);
 std::cout << "All work has completed\n";</pre>
```

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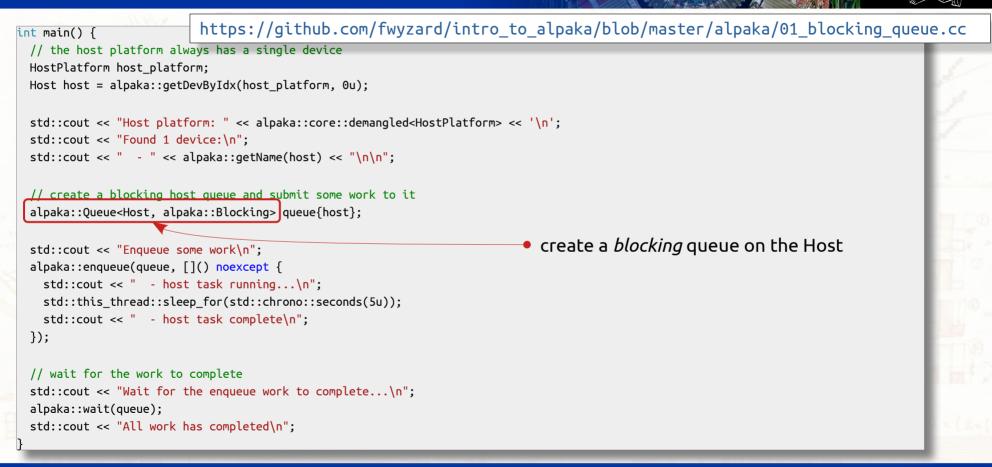




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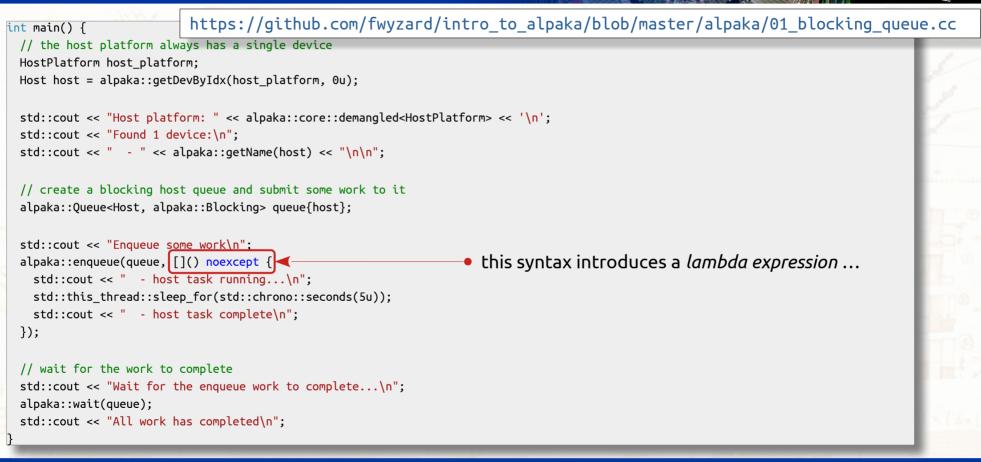




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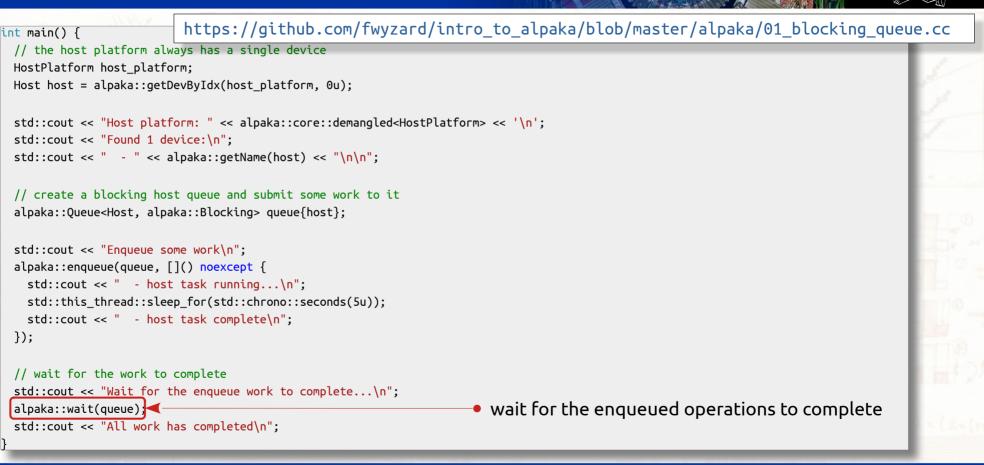


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 HostPlatform host platform;
 Host host = alpaka::getDevByIdx(host platform, 0u);
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 std::cout << "Found 1 device:\n":</pre>
 std::cout << " - " << alpaka::getName(host) << "\n\n";</pre>
 // create a blocking host queue and submit some work to it
 alpaka::Queue<Host, alpaka::Blocking> gueue{host};
 std::cout << "Enqueue some work\n";</pre>
                                                                     this syntax introduces a lambda expression
 alpaka::enqueue(queue, []() noexcept {
   std::cout << " - host task running...\n";</pre>
                                                                     that performs these operations
   std::this thread::sleep for(std::chrono::seconds(5u));
   std::cout << " - host task complete\n";</pre>
 });
                                                                     togethwer with alpaka::engueue(...), this part
 // wait for the work to complete
                                                                        - creates an object that encapsulates some operations
 std::cout << "Wait for the enqueue work to complete...\n";</pre>
 alpaka::wait(queue);
                                                                        - submits those opertations to run in a queue
 std::cout << "All work has completed\n";</pre>
```

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let's build it and run it O



- in this example we are not making use of any accelerator
 - let's build it only for the CPU back-end

g++ -DALPAKA_ACC_CPU_B_SEQ_T_SEQ_ENABLED \
 -std=c++17 -02 -g -I\$ALPAKA_BASE/include \
 01_blocking_queue.cc \
 -o 01_blocking_queue_cpu

```
and run it
```

\$./01_blocking_queue_cpu
Host platform: alpaka::PltfCpu
Found 1 device:

- AMD EPYC 7352 24-Core Processor

Enqueue some work

- host task running...
- host task complete
- Wait for the enqueue work to complete...

```
All work has completed
```

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an async example



```
https://github.com/fwyzard/intro_to_alpaka/blob/master/alpaka/02_nonblocking_queue.cc
int main() {
 // the host platform always has a single device
 HostPlatform host platform;
 Host host = alpaka::getDevByIdx(host platform, 0u);
 std::cout << "Host platform: " << alpaka::core::demangled<HostPlatform> << '\n';</pre>
 std::cout << "Found 1 device:\n";</pre>
 std::cout << " - " << alpaka::getName(host) << "\n\n";</pre>
 // create a non-blocking host queue and submit some work to it
 alpaka::Queue<Host, alpaka::NonBlocking> gueue{host};
 std::cout << "Enqueue some work\n";</pre>
 alpaka::enqueue(queue, []() noexcept {
   std::cout << " - host task running...\n";</pre>
   std::this thread::sleep for(std::chrono::seconds(5u));
   std::cout << " - host task complete\n";</pre>
 });
 // wait for the work to complete
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```

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an async example



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 std::cout << "Found 1 device:\n":</pre>
 std::cout << " - " << alpaka::getName(host) << "\n\n";</pre>
 // create a non-blocking host gueue and submit some work to it
 alpaka::Queue<Host, alpaka::NonBlocking> queue{host};

    create a non-blocking queue on the Host

 std::cout << "Enqueue some work\n";</pre>
 alpaka::enqueue(queue, []() noexcept {
   std::cout << " - host task running...\n";</pre>
   std::this thread::sleep for(std::chrono::seconds(5u));
   std::cout << " - host task complete\n";</pre>
 });
 // wait for the work to complete
 std::cout << "Wait for the enqueue work to complete...\n";</pre>
 alpaka::wait(queue);
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```

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let's build it and run it O



- in this example, too, we are not making use of any accelerator
 - let's build it only for the CPU back-end with POSIX threads
 - g++ -DALPAKA_ACC_CPU_B_SEQ_T_SEQ_ENABLED \ -std=c++17 -02 -g -I\$ALPAKA_BASE/include -pthread \ 02_nonblocking_queue.cc \ -0 02_nonblocking_queue_cpu

```
and run it
```

\$./02_nonblocking_queue_cpu
Host platform: alpaka::PltfCpu
Found 1 device:

- AMD EPYC 7352 24-Core Processor

Enqueue some work

Wait for the enqueue work to complete...

- host task running...
- host task complete

All work has completed

```
November 5<sup>th</sup>, 2024
```





blocking vs non-blocking



\$./01_blocking_queue_cpu Host platform: alpaka::PltfCpu Found 1 device:

- AMD EPYC 7352 24-Core Processor

Enqueue some work

- host task running...
- host task complete

Wait for the enqueue work to complete... All work has completed \$./02_nonblocking_queue_cpu Host platform: alpaka::PltfCpu Found 1 device:

- AMD EPYC 7352 24-Core Processor

Enqueue some work Wait for the enqueue work to complete... - host task running...

- host task complete
- All work has completed

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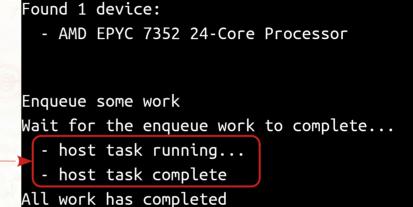
blocking vs non-blocking

Ś



./01_blocking_queue_cpu	
Host platform: alpaka::PltfCpu	
Found 1 device:	
- AMD EPYC 7352 24-Core Processor	
Enqueue some work	
- host task running	
- host task complete	
Wait for the enqueue work to complete	
All work has completed	

- with a synchronous (or *blocking*) queue:
 - any operation is executed immediately, before returning to the caller
 - the host automatically waits (blocks) until each operation is complete
 - with an asynchronous (or *non-blocking*) queue:
 - any operation is executed in the background, and each call returns immediately, without waiting for its completion
 - the host needs to synchronize explicitly with the queue, before accessing the results of the operations



./02_nonblocking_queue_cpu

Host platform: alpaka::PltfCpu

November 5th, 2024



what's next?



summary

- today we have learned
 - what *performance portability* means and discovered the Alpaka library
 - how to set up Alpaka for a simple project
 - how to compile a single source file for different back-ends
 - what are Alpaka platforms, devices, queues and events
- in the next part we will see
 - how to work with host and device memory
 - how to write device functions and kernels
 - how to use an Alpaka accelerator and work division to launch a kernel
 - a complete example !



November 5th, 2024

(more) questions?



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